ST8000A TECHNICAL MANUAL CHAPTER 4, THEORY OF OPERATION Second Draft, September 17, 1990 HAL/900917a

This draft version has the following changes:

1. All references in Section 4.2 (MODEM) have been revised to show component designations used on the <u>9-11-90</u> set of schematic drawings.

2. Table 4.4 (AMH DELAY SETTINGS) has been revised.

3. New text by Drew White for Section 4.3 (CONTROL) has been added. Component references agree with the 9-05-90 set of schematic drawings.

4. Changes to Drew White's text for 4.3:

a. All Tables have been given formal table numbers and names.

b. Seven Tables in Section 4.3.6.4 (CONTROL REGISTER 1) have been combined into Table 4.8.

c. Six Tables in Section 4.3.6.5 (CONTROL REGISTER 2) have been combined into Table 4.20.

d. A reference to Figure 4.14 (CLOCK RECOVERY CIRCUITS) has been added to Section 4.3.14.

e. References to Schematic diagrams have been changed from "Figure A1806" to "Schematic A1806" format. These will then be converted to Chapter 6 (or 7) figures (6.1, 6.2 etc.) as that section of the manual develops. The "Schematic" labels will be used in a "find and revise" search when Chapter 6 is formalized.

5. Page numbering has been changed to the "4-#" format.

6. The Table of Contents, Tables, and Figures has been revised to include page numbers and up-dated to include actual Section, Table, and Figure titles.

7. Note the date code at the lower left-hand margin of each page - "HAL/900917a". I will use this in-house to distinguish between various versions as the manual develops.

8. Nomenclature Note: The Schematic diagrams use the format "INT.L" to designate an active-low data signal (NOT-INT). Drew White's nomenclature uses "INT_L". We will need to be consistent in the final version ("INT.L" agreed 9/17/90).

9. As of this date, **ALL** sections of Chapter 4 are now complete - at least for this <u>second draft</u>. Front panel component designators <u>will</u> change as the circuit board layout progresses.

GWH